



Embedded Systems

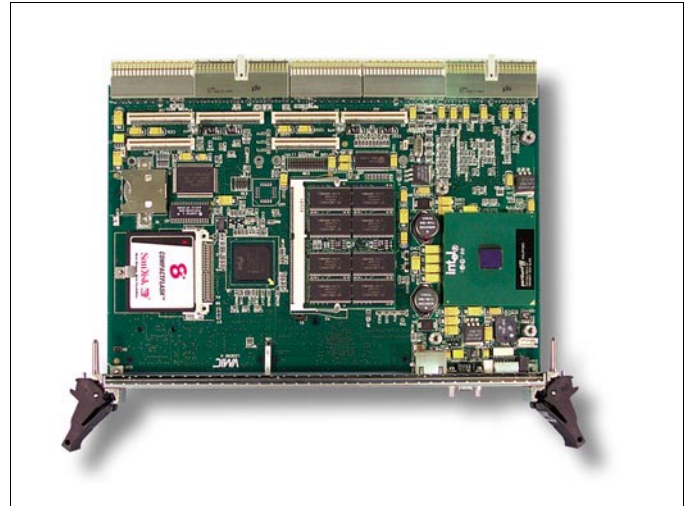
VMICPCI-7755

Intel® Pentium® III Processor-Based CompactPCI® System Controller SBC with 133 MHz System Bus

- **Single slot Pentium® III FC-PGA370 socket processor-based single board computer (SBC) with 133 MHz system bus**
- **Up to 512 Mbyte IDE CompactFlash (optional)**
- **Four programmable timers (two 16-bit, two 32-bit)**
- **32 Kbyte of nonvolatile SRAM**
- **Remote Ethernet booting**
- **Software-selectable watchdog timer with reset**
- **Dual PMC expansion sites**
- **CompactPCI® 2.0 revision 3.0-compliant system slot controller using Intel® 21154 PCI-to-PCI bridge (64-bit/33 MHz)**
- **Pentium III FC-PGA370 socket processor with speeds up to 1 GHz**
- **Up to 512 Mbyte PC-133 SDRAM using one 144-pin SODIMM module**
- **Integrated 2D/3D graphics (built-in VGA maximum resolution (8 bpp at 1,600 x 1,200, 16 bpp at 1,600 x 900, and 24 bpp at 1,280 x 1,024))**
- **Onboard Fast Ethernet controller supporting 10BaseT and 100BaseTX interfaces**
- **Onboard Ultra DMA/100 hard drive and floppy drive controllers with CompactPCI backplane I/O**
- **Two high performance 16550-compatible serial ports. Both serial ports available through CompactPCI backplane connectors.**
- **One USB port available via the CompactPCI backplane connectors**
- **PS/2-style keyboard and mouse port on front panel**
- **Real time clock and miniature speaker included**
- **Passive heat sink**
- **Operating system support for Windows NT® and Windows NT Embedded, Windows® 2000, VxWorks®, Solaris™, Linux®, LynxOS® and QNX®**

Functional Characteristics

Microprocessor: The VMICPCI-7755 brings the Intel Pentium III processor to CompactPCI bus, offering processor speeds up to 1 GHz. The Pentium III processor has 32-bit addressing and a 64-bit data bus. Its superscalar architecture allows three instructions to be executed per clock cycle. A dynamic branch prediction unit, separate instruction and data caches, and MMX™ technology also increase the processor's performance. The Pentium III processor also provides 256 Kbyte of advanced transfer cache (on-die, full speed level 2 cache) using dual independent bus architecture for high bandwidth and performance. This



L2 cache operates at the same clock frequency as the processor, thus improving performance.

Super VGA Controller¹: High-resolution graphics and multimedia-quality video are supported on the VMICPCI-7755 by an internal 815E AGP graphics controller. The controller uses 4 Mbyte synchronous DRAM display cache with a high-bandwidth 64-bit data interface and supports screen resolutions up to 1,600 x 1,200 x 64 K colors.

DRAM Memory: The VMICPCI-7755 accepts one 144-pin SODIMM PC-133 SDRAM module for a maximum memory capacity of 512 Mbyte. The onboard DRAM is dual ported to the CompactPCI bus.

BIOS: System BIOS, video BIOS, and LAN Boot BIOS are provided in reprogrammable flash memory. BIOS loader software can be provided.

Ethernet Controller: The VMICPCI-7755 supports Ethernet LANs with the Intel 82559ER PCI Ethernet controller. 10BaseT and 100BaseTX options are supported via an RJ45 connector.

Remote Ethernet Booting: The VMICPCI-7755 utilizes Lanworks Technologies, Inc.'s BootWare BIOS. BootWare provides the ability to remotely boot the VMICPCI-7755, and provides the following features:

¹ The VMICPCI-7755 requires an adapter cable (available separately) to adapt the board's micro-DB9 connector to the standard VGA high-density DB15 connector.

Ordering Options						
June 5, 2002 800-657755-000 F	A	B	C	D	E	F
VMICPCI-7755	—				0	0
<p>A = Processor 0 = Reserved 1 = Reserved 2 = Reserved 3 = Reserved 4 = 733 MHz Pentium III SBC 5 = Reserved 6 = 866 MHz Pentium III SBC 7 = Reserved 8 = 1 GHz Pentium III SBC</p> <p>B = SDRAM Memory 0 = Reserved 1 = Reserved 2 = Reserved 3 = 32 Mbyte 4 = 64 Mbyte 5 = 128 Mbyte 6 = 256 Mbyte 7 = 512 Mbyte</p> <p>C = CompactFlash Drive 0 = None 1 = 8 Mbyte 2 = Reserved 3 = Reserved 4 = Reserved 5 = 64 Mbyte 6 = Reserved 7 = 96 Mbyte 8 = 128 Mbyte 9 = 160 Mbyte G = 192 Mbyte H = 256 Mbyte J = 512 Mbyte</p> <p>D = Unterminated Parallel Port Connection to Backplane 0 = J4 Not Loaded 1 = J4 Loaded</p> <p>EF = 0 (Options reserved for future use.)</p>						
CompactPCI Rear Transition Utility Board						
VMIACC-0577						
The VMIACC-0577 mounts in the rear transition area of the backplane and provides access to the serial, USB, enhanced IDE, floppy disk, and PMC I/O connectors by way of the J3 and J5 connectors. The VMIACC-0577 is sold separately.						
Connector Adapter Kit						
360-000168-004						
A connector adapter, part number 360-000168-004, is available to adapt the product's front panel, 9-pin video port Micro-D connector to a Standard-D 15-pin connector. This adapter is sold separately.						
For Ordering Information, Call: 1-800-322-3616 or 1-256-880-0444 • FAX (256) 882-0859 Email: info.embeddedsystems@gefanuc.com Web Address: www.gefanuc.com/embedded Copyright © 2004 by VMIC Specifications subject to change without notice.						

- NetWare (802.2, 802.3, or Eth II), TCP/IP (DHCP or BootP), and PXE boot support
- Unparalleled boot sector virus protection
- Detailed boot configuration screens
- Comprehensive diagnostics
- Optional disabling of local boots
- Dual-boot option lets users select network or local booting

Serial Ports: Two 16550-compatible serial ports are featured on the VMICPCI-7755. Each serial channel has an independent 16-byte FIFO to support baud rates up to 115 kHz.

USB Port: The VMICPCI-7755 provides one Universal Serial Bus (USB) port. The port is available at the CompactPCI backplane connector.

Keyboard and Mouse Ports: The VMICPCI-7755 supports a combined PS/2 keyboard and mouse connector for peripherals. A **Y**-adapter cable is supplied with the unit.

Flash Memory: The VMICPCI-7755 provides up to 512 Mbyte of CompactFlash memory accessible through the secondary IDE port. The VMICPCI-7755 BIOS includes an option to allow the board to boot from the flash memory.

The onboard flash emulates a hard disk drive. Accesses to flash are identical to file operations for a hard disk drive. Because of the hard disk emulation feature, no OS-specific software drivers are required for flash memory operation.

Programmable Timers: The VMICPCI-7755 provides the user with two 16-bit timers and two 32-bit timers. These timers are mapped in PCI memory space, are completely software programmable, and can generate PCI interrupts.

Watchdog Timer: The VMICPCI-7755 provides a software-programmable watchdog timer. The watchdog timer is enabled under software control. Once the watchdog timer is enabled, onboard software must access the timer within the specified timer period, or the output of the watchdog timer will reset the unit.

Nonvolatile SRAM: The VMICPCI-7755 provides 32 Kbyte of nonvolatile SRAM. The contents of the SRAM are preserved when +5 V power is interrupted or removed from the unit.

Reset Switch and Annunciators: A small push-button switch on the front panel will reset the VMICPCI-7755. Only the onboard VMICPCI-7755 logic will be reset. There will be no reset generated on the CompactPCI bus. Six LEDs are visible on the front panel: +5 V power, board status, IDE activity, LAN activity, LAN mode (10 or 100 MHz mode), and temperature status. A small speaker is also included on the VMICPCI-7755 to provide sound output.

PMC Expansion Site: The VMICPCI-7755 supports the IEEE P1386 common mezzanine card specification with two 5 V PCI mezzanine card (PMC) expansion sites. PMC I/O is routed to the CompactPCI backplane connectors.

The following is a partial list of commercially available PMC modules:

- Fibre Channel
- Analog and digital I/O
- High speed serial and parallel I/O
- Networking adapters: FDDI, ATM, Ethernet, Fast Ethernet
- PMC-to-PC Card adapter
- MIL-STD-1553 bus interface
- SRAM
- Flash
- Solid-state disk
- Data acquisition cards
- SCSI-2 adapter
- Parallel links
- Octal DSP
- Quad SIO
- GPIB
- FAX/modem
- PMC-to-PMC expanders
- TI/EI and serial communications interfaces

Contact GE Fanuc Embedded Systems for more information concerning PMC modules and compatibility.

CompactPCI bus Bridge: The VMICPCI-7755 is fully compliant with CompactPCI Rev 2.1, PCI 2.1, and PCI-to-PCI Bridge Architecture Rev. 1.1. This system slot board provides the following features for adapters loaded in the CompactPCI subsystem.

- 64-bit/33 MHz CompactPCI interface

- Implements delayed transactions for all PCI configuration, I/O, and memory read commands — up to three transactions simultaneously in each direction
- Allows 152 bytes of buffering (data and address) for upstream posted memory write commands and 88 bytes of buffering for downstream posted memory write commands — up to nine upstream and five downstream posted write transactions simultaneously
- Allows 152 bytes of read data buffering upstream and 152 bytes of read data buffering downstream
- Provides concurrent primary and CompactPCI bus operation to isolate traffic
- Provides seven CompactPCI clock outputs:
 - Low skew, permitting direct drive of option slots
 - Individual clock disables, capable of automatic configuration during reset
- Provides arbitration support for seven CompactPCI bus devices; a programmable two-level arbiter
- Provides enhanced address decoding:
 - A 32-bit I/O address range
 - A 32-bit memory-mapped I/O address range
 - A 64-bit prefetchable memory address range
 - ISA-aware mode for legacy support in the first 64 Kbyte of I/O address range
 - VGA addressing and VGA palette snooping support for VGAs located on CompactPCI bus
 - Supports PCI transaction forwarding for the following commands:
 - All I/O and memory commands
 - Type 1 to type 1 configuration commands
 - Type 1 to type 0 configuration commands (downstream only)
 - All type 1 to special cycle configuration commands
- Includes downstream lock support
- Supports either 5 or 3.3 V signaling environments

Operating System and Software Support

GE Fanuc has a wide range of software products designed to run on Intel processor-based SBCs, such as the VMICPCI-7755. These products are aimed at developers who are incorporating GE Fanuc SBCs, I/O boards, and workstations into systems.

Windows NT OS Support — VMISFT-9421 Generic Device

Driver: The VMISFT-9421 is very useful in those cases where the driver for peripheral devices is not available.

The generic hardware driver is a set of functions and utilities that makes it possible to easily develop, debug, and run standard

Windows NT applications that access any PC Card (PCMCIA), PMC, and CompactPCI devices.

Visual Basic[®], C/C++, and any other language that can call functions in a dynamic link library or connect to a DDE server are supported.

VxWorks OS Support — VMISFT-7418 Board Support Package:

The VMISFT-7418, VxWorks board support package (BSP), is a Wind River Systems, Inc. certified BSP for GE Fanuc's series of CompactPCI Pentium processor-based computers that is required to run the VxWorks OS. Complete source code is provided, thus giving the user the utmost flexibility in system reconfiguration. Along with the Wind River Systems, Inc.-supplied software, a VxWorks OS kernel can be built and rebuilt for specific applications. For example, the user can easily reconfigure standard output to be via serial port or SVGA.

VMISFT-7418 is linked by the user with VxWorks OS, thus allowing software applications created with Wind River Systems, Inc.'s development system to load and run on the particular GE Fanuc SBC hardware being used. Serial ports, parallel ports, keyboard, text mode video, and Ethernet are all supported, as well as floppy and IDE hard disk drives that can be connected to the computer boards. The BSP also provides Flash boot, NVRAM, and timer support.

VMISFT-7418 allows VxWorks applications to have access to the CompactPCI bus including memory, I/O, and configuration cycles. The BSP provides interprocessor communications capability via shared memory. The interprocessor communications provides system slot SBC to peripheral-slot communications via the CompactPCI bus.

QNX, Solaris, Linux and LynxOS: QNX, Solaris, Linux, and LynxOS are supported by VMICPCI-7xxx without the need for additional software drivers. The timers and battery-backed RAM may be accessed directly by application code.

Physical/Environmental Specifications

Dimensions: 6U single slot Eurocard form factor

Height	9.2 in. (233.4 mm)
Depth	6.3 in. (160 mm)
Thickness	0.8 in. (20.3 mm)

Power Requirements:

- +5 VDC (±5 percent), 5 A (typical), 6 A maximum
- +3.3 VDC, 3 A (typical), 8 A maximum
- +12 VDC (±5 percent), 100 mA (typical), 500 mA maximum
- 12 VDC (±5 percent), 50 mA (typical), 100 mA maximum

Note: The currents at +12 and -12 VDC are specified with the serial connectors open. The currents at +5 and +3.3 VDC are specified with no PMCs installed.

Temperature:

- Operating: 0 to +50 °C
(Air flow requirement as measured at output side of heatsink is to be greater than 350 LFM)
- Storage: -25 to +80 °C

Relative Humidity: 10% to 90%, noncondensing

PMC Expansion Site Connector:

- 5 V signaling, types 1 and 2
- 32-bit PCI bus, 33 MHz maximum

MTBF: 167,574 hours (Bellcore)

Compatible Products

PMC Expansion Sites: GE Fanuc supports PMC via the two onboard PMC expansion sites. These expansion sites allow the VMICPCI-7755 to take advantage of the many commercially available PMC boards from third-party sources and GE Fanuc.

Trademarks

MMX is a trademark and Intel and Pentium are registered trademarks of Intel Corporation. CompactPCI is a registered trademark of PCI Industrial Computer Manufacturers' Group. Visual Basic, Windows, and Windows NT are registered trademarks of Microsoft Corporation. Other registered trademarks are the property of their respective owners.

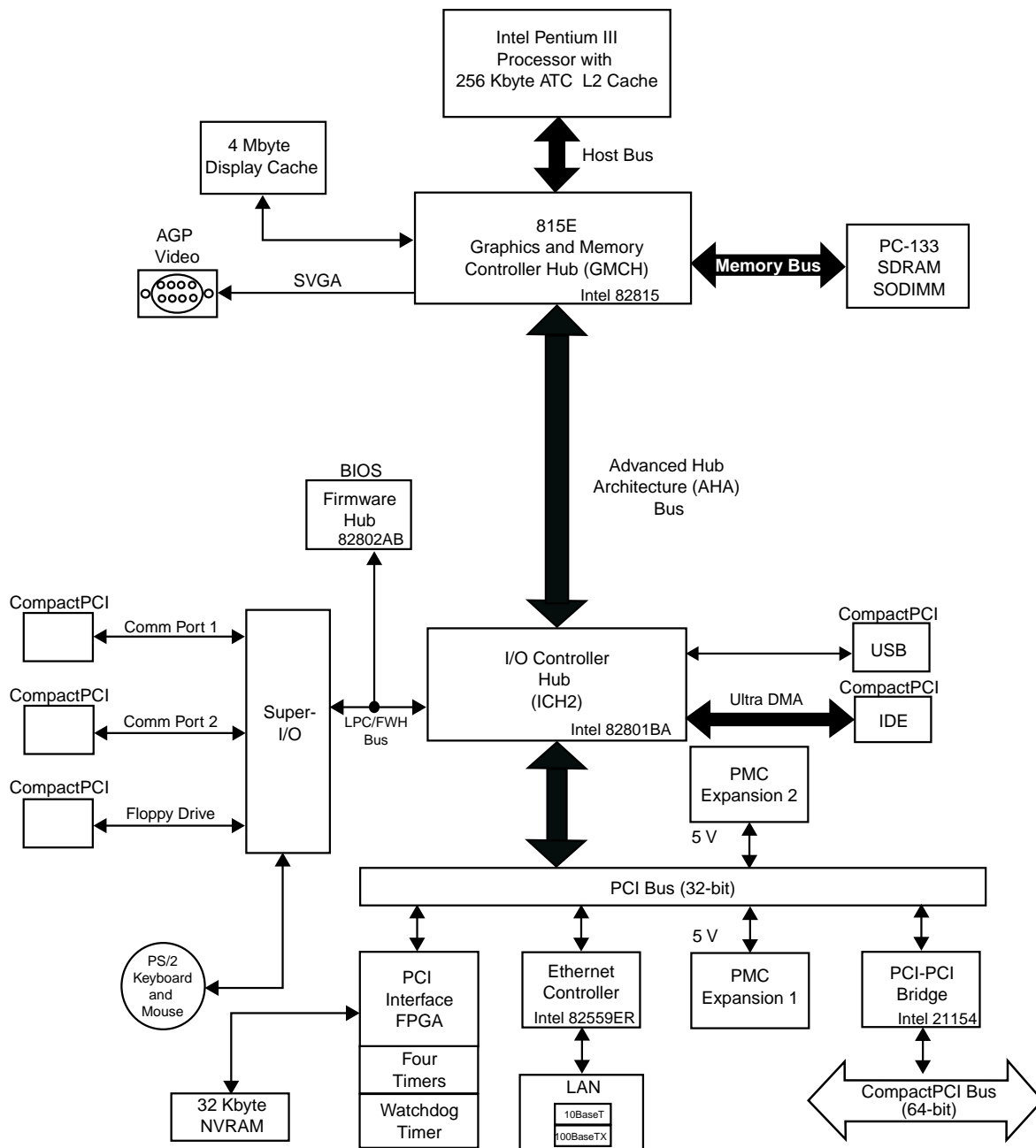


Figure 1. VMICPCI-7755 Block Diagram



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 Paris, France 33 (1) 4324 6007

Additional Resources

For more information, please visit the GE Fanuc Embedded Systems web site at:
www.gefanuc.com/embedded